

## Preprocessor Interfaces For Hewlett-Packard Logic Analyzers

- Easy HP Logic Analysis Connection to MCS Systems
- Configure HP Logic Analyzers For MCS trace
- Label HP Logic Analyzer Inputs For MCS Signals
- View MCS Timing Analysis Up to 500 MHz
- Examine MCS State Analysis
- Disassemble MCS Bus Activity Into Mnemonics
- Filter Disassembly Display For Maximum Information

The Hewlett-Packard Intel MCS® microcontroller preprocessor interfaces provide an easy way to connect an HP 16500 Modular Logic Analysis System or a HP 1660 and HP 1670 series logic analyzer to a target system using Intel MCS microcontrollers. All necessary clocks are passed to the logic analyzer by the preprocessor ensuring that data is captured at the correct time.

Software is shipped with the preprocessors that automatically configures the logic analyzer, and generates labels for MCS microcontroller address, data, and status signals. Included with the software is a disassembler that displays execution traces in Intel MCS microcontroller mnemonics. Instructions that are prefetched but not executed are marked in the trace display. Preprocessor interfaces for Intel MCS controllers provide disassembly filters to speed debugging by showing or suppressing controller cycles in the trace listing. Two operating modes are available: state-per-clock, which provides a complete display of all bus activity; and timing analysis up to 500 MHz (channel-to-channel skew is 1 ns).

Combine the HP 16500 Modular Logic Analysis System with the HP 16505A Prototype Analyzer to capture and analyze real-time data from Intel MCS microcontroller systems. Track tough system problems to root cause by viewing data in domains ranging from source code view to signal integrity view with the HP 16505A Prototype Analyzer. By using the HP 16505A design team members can see fully time-correlated measurement data in the format most suited to their discipline.

### MICROCONTROLLERS SUPPORTED:

HP E2415B Preprocessor: 8x3x, 8xC3x, 8x5x, 8xC5x, 8xC5xFx, 8xL5xFx and 8xC251SQ/SA/SB in 40-pin DIP and 44-pin PLCC packages (support for 44-pin PLCC requires adapter from Emulation Technology).

HP E2416B Preprocessor: 80196KA, 80196KB, 80196KC, 80196KC, and 80196KD in 68-pin PLCC. Maximum bus clock frequency supported is 20 MHz.

### LOGIC ANALYZERS SUPPORTED:

HP 16500B logic analyzer mainframe; HP 16550A, HP 16554A, HP 16555A, HP 16556A (state and timing cards for the HP 16500B mainframe); HP 16505A Prototype Analyzer; HP B4620A Software. Analyzer Toolset and HP 1660C/CS, HP 1661C/CS, HP 1662C/CS, HP 1670A, HP 1671A, and HP 1672A portable logic analyzers

### AVAILABILITY:

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### CONTACT:

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