

PowerPack* 386 In-Circuit Emulator

- Configurable From 2.7V to 5.5V
- 25 MHz, Zero Wait State Operation
- Up to 4 Megabyte Overlay Memory
- Intuitive Windows 3.1 SLD Source-Level Debugger
- Clock-Edge Triggers and Trace
- Up to 256 Trace Buffers and 256K Frames
- Eight Complex Events Define Four Sequential Triggers
- Uses Intel Bondout Technology
- Exhaustive Self-Test Board Diagnostics
- Long Cable and a Small Probe Head

Microtek's PowerPack* 386 in-circuit emulator helps you to exploit the advantages of the Intel386™ architecture. Intel's licensed bondout technology makes it possible to support clock-edge triggering and display. You can use up to 256 separate trace buffers and 256K frames to pinpoint full-speed events.

The intuitive Windows 3.1 interface provides state-of-the-art source level debug and full control of the emulator. The source window displays code as source, assembly or mixed with simultaneous views of multiple source files. Point and click to browse functions, set breakpoints, single-step or interrogate local variables. The peripheral window selectively shows internal peripheral units, registers within units and fields within registers.

Control execution with go, halt, goto cursor, go until/into call, return, step over/into call, single step by line or statement. Set a breakpoint on an individual statement within a line. The variable window continuously monitors global or stack variables. Unlike ROM-monitors which single-step to display status, emulation displays executed code, not pre-fetched instructions, while the target runs full-speed.



Define eight events on address, data, processor status, I/O and peripheral signals. Ranges, bit masking and negation apply to both address and data. Combine these with two 20-bit counter/timers to set 4 sequential triggers. These trigger breakpoints, external signals and capture data into multiple trace buffers with clock-edge resolution.

Full-speed trace collects data on clock cycles instead of the more conventional bus cycles to detail memory wait states and peripheral timing. Up to 256 separate trace buffers and 256K frames make it possible to record multiple instances of well-defined, rare events.

A stand-alone self-test board and supporting software provides a comprehensive confidence test for the entire system. The board also includes a small prototyping area. The long 48" flexible cable and small 2.2" x 3.3" x 1.1" probe head fits into small spaces.

HOST SYSTEMS SUPPORTED:

PC with Microsoft Windows 3.1, Ethernet support for Sun PC/NFS and OS2/LAN Manager

PROCESSORS SUPPORTED:

Intel386 EX/SX/CX Processors

AVAILABILITY:

Now

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 IN-CIRCUIT EMULATORS