

Single-Layer Feed Waveguide Consisting of Posts for Plane TEM Wave Excitation in Parallel Plates

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Abstract—The authors propose a novel feed structure to excite a plane TEM wave in a parallel-plate waveguide. The feed waveguide is composed of densely arrayed posts on the same layer as the parallel plate. The posts can be easily fabricated at low cost by making metalized via holes in a grounded dielectric substrate. Such a procedure results in a quite simple fabrication of the antenna. The feed waveguide is designed to obtain a uniform division, which is confirmed by measurements on a 40-GHz band model.

Index Terms—Parallel-plate waveguides.

I. INTRODUCTION

A parallel-plate slot array antenna is an attractive candidate for high-efficiency and mass-produceable planar phased-array antennas [1], [2] for millimeter-wave applications. In general, the transmission loss in a waveguide is very small in comparison with other feedlines such as a microstrip line and a suspended line. The radiating part has a simple structure that consists of slots etched on the upper plate. A number of various techniques to excite a plane TEM wave in the parallel-plate waveguide have been reported in the literature [3]–[10]. Feed structures using a horn [7] and a parabolic reflector [8] are simple, but need relatively large areas when placed on the same layer as the radiating parts. In order to reduce the antenna size, the feed structure should be attached to the bottom of the radiating part and connected to it by a bend. A feed structure using a nonradiative dielectric (NRD) guide [9] raises the same problem since it is difficult to provide radiation in the direction perpendicular to the guide. The authors previously proposed a waveguide-fed parallel-plate slot array antenna [10] in which a rectangular feed waveguide was attached to the bottom of the parallel-plate waveguide. These feed structures are a bit complicated from the manufacturing point of view.

In this paper, we introduce a novel single-layer structure for a feed waveguide, as shown in Fig. 1. The walls of the waveguide are equivalently composed by densely arrayed posts. The structure is advantageous in terms of practical fabrication of planar antennas for millimeter-wave applications such as wireless LAN (59–60 GHz) and car collision avoidance radar (76.5–77.5 GHz). For example, the antenna can be easily fabricated at low cost by making many via holes in

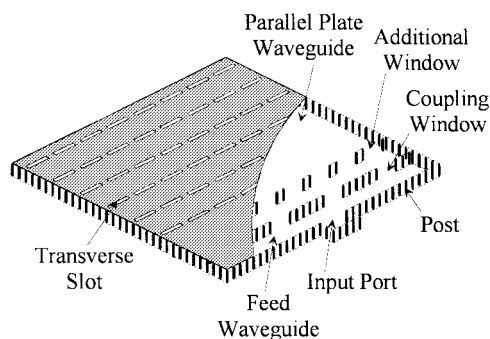


Fig. 1. Parallel-plate slot array with a single-layer feed waveguide.

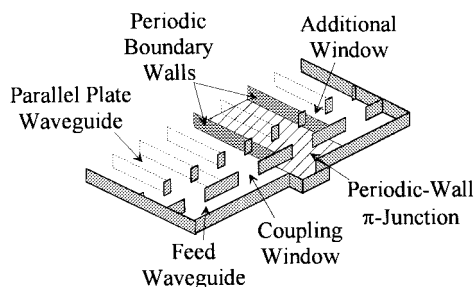


Fig. 2. Single-layer feed waveguide to excite plane TEM wave.

a grounded dielectric substrate and metalizing them as posts. Section II explains the structure and the operational principle of the single-layer feed waveguide exciting a plane TEM wave in a parallel-plate waveguide. In Section III, we analyze the propagation constant of a waveguide consisting of posts (called thereafter a post-wall waveguide) as a function of their spacing and diameter. Section IV presents a moment-method analysis of a unit component of the feed waveguide (named a periodic-wall π junction), while Section V shows the characteristics of the component. Next, the design procedure of the feed waveguide to obtain uniform power division is described in Section VI. Section VII confirms the uniform division of the feed waveguide by measurements on a 40-GHz band model. In Section VIII, we present problems that still have to be solved in a future study. Finally, Section IX is devoted to summarizing the results presented in this paper.

II. STRUCTURE

Fig. 2 shows the configuration of the structure based on the feed waveguide of a single-layer slotted waveguide array [11], [12]. It is placed on the same layer as the radiating rectangular

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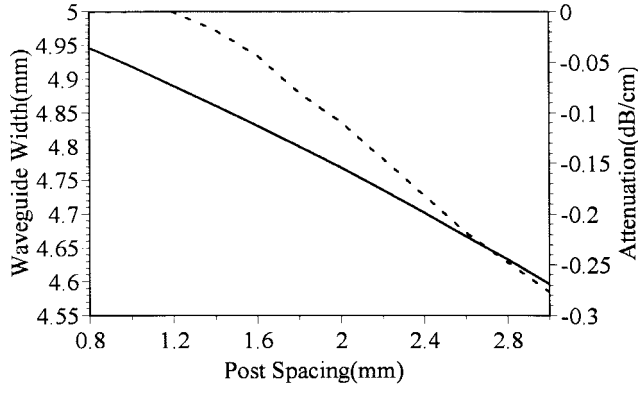


Fig. 5. Equivalent waveguide width and attenuation coefficient. — equivalent waveguide width; ··· attenuation coefficient.

TABLE I
PARAMETERS OF THE FEED WAVEGUIDE

Equivalent Width of a Feed Waveguide	a_e	4.43mm
Spacing of Periodic Boundary Walls	a_r	6.30mm
Height of Parallel Plate Waveguide	b	1.52mm
Dielectric Constant	ϵ_r	2.17
Post Diameter	d	0.60mm
Distance of Additional Windows	h	1.70mm
Distance of a Short Circuit	e	2.50mm
Number of Pins per Post	I	12
Number of Basis Functions for Magnetic Current in Apertures		5
Frequency	f	40.0GHz

small for the post spacing up to 1.2 mm, i.e., twice the post diameter.

IV. ANALYSIS OF A PERIODIC WALL π JUNCTION

Let us consider the periodic wall π junction shown in Fig. 6 for incidence of the TE_{10} mode from Port 1. When the structure is 2-D and uniform to the y direction, the field in the junction can be expanded in terms of TE_{n0} modes. The analysis model is divided into three regions by placing PEC walls together with equivalent magnetic currents M_1, M_2 on the two apertures of the coupling windows according to the field equivalence theorem. The three regions are the feed waveguide (Region I), the wall-thickness (Region II), and the periodic wall waveguide (Region III). Several uniform electric currents are assumed on the surfaces of the reflection-canceling post ($J_{rj}: j = 1 - I_r$), the waveguide post ($J_{tj}: j = 1 - I_t$), and the window post ($J_{wj}: j = 1 - I_w$), respectively [15], [16]. Next, applying the continuous conditions of the magnetic field on the window apertures (S_1 and S_2) and the null conditions of electric field on the PEC post surfaces (S_{ri}, S_{ti} and S_{wi}), we derive the coupled integral equations for electric and magnetic currents.

On S_{ri}

$$E_{in} + \sum_{j=1}^{I_r} \int_{S_{rj}} ds_s \bar{G}_{Ie}^e \cdot J_{rj} + \int_{S_1} ds_s \bar{G}_{Im}^e \cdot M_1 = 0. \quad (5)$$

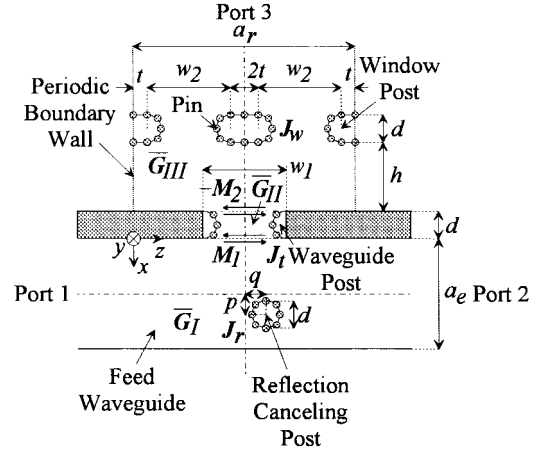


Fig. 6. Analysis model of a periodic wall π junction.

On S_1

$$\begin{aligned} H_{in} + \sum_{j=1}^{I_r} \int_{S_{rj}} ds_s \bar{G}_{Ie}^m \cdot J_{rj} + \int_{S_1} ds_s \bar{G}_{Im}^m \cdot M_1 \\ = \int_{S_1} ds_s \bar{G}_{IIIm}^m \cdot (-M_1) + \sum_{j=1}^{I_t} \int_{S_{tj}} ds_s \bar{G}_{IIe}^m \cdot J_{tj} \\ + \int_{S_2} ds_s \bar{G}_{IIIm}^m \cdot M_2. \end{aligned} \quad (6)$$

On S_{ti}

$$\begin{aligned} \int_{S_1} ds_s \bar{G}_{IIIm}^e \cdot (-M_1) + \sum_{j=1}^{I_t} \int_{S_{tj}} ds_s \bar{G}_{IIe}^e \cdot J_{tj} \\ + \int_{S_2} ds_s \bar{G}_{IIIm}^e \cdot M_2 = 0. \end{aligned} \quad (7)$$

On S_2

$$\begin{aligned} \int_{S_1} ds_s \bar{G}_{IIIm}^m \cdot (-M_1) + \sum_{j=1}^{I_t} \int_{S_{tj}} ds_s \bar{G}_{IIe}^m \cdot J_{tj} \\ + \int_{S_2} ds_s \bar{G}_{IIIm}^m \cdot M_2 \\ = \int_{S_2} ds_s \bar{G}_{IIIIm}^m \cdot (-M_2) + \sum_{j=1}^{I_w} \int_{S_{wj}} ds_s \bar{G}_{IIIe}^m \cdot J_{wj}. \end{aligned} \quad (8)$$

On S_{wi}

$$\begin{aligned} \int_{S_2} ds_s \bar{G}_{IIIIm}^e \cdot (-M_2) \\ + \sum_{j=1}^{I_w} \int_{S_{wj}} ds_s \bar{G}_{IIIe}^e \cdot J_{wj} = 0. \end{aligned} \quad (9)$$

In (5)–(9), E_{in} and H_{in} are the electric and magnetic field of the incident wave, while the dyadic Green's functions \bar{G} 's are referred to as the following:

\bar{G}_{ke}^e for electric field produced by a unit electric current in Region k ($k = \text{I, II, and III}$);

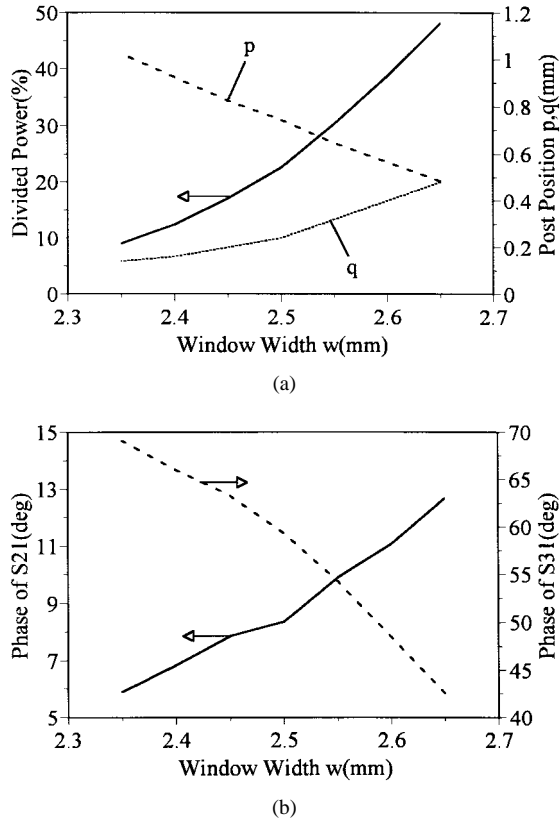


Fig. 7. Characteristics as functions of window width w_1 . (a) Divided power (%) and post position (p, q). (b) Phase of S_{21} and S_{31} (deg).

- \bar{G}_{ke}^m for magnetic field produced by a unit electric current in Region k ($k = \text{I, II, and III}$);
- \bar{G}_{km}^e for electric field produced by a unit magnetic current in Region k ($k = \text{I, II, and III}$);
- \bar{G}_{km}^m for magnetic field produced by a unit magnetic current in Region k ($k = \text{I, II, and III}$).

In the dyadic Green's functions of the periodic wall waveguide (Region III), the x dependence of the modes is expressed in terms of Fourier series with a period of a_r [14]. Galerkin's method of moments has then been applied to solve (5)–(9), where \mathbf{M}_1 and \mathbf{M}_2 have been expanded in terms of z -directed sinusoidal entire-domain basis functions [12]. Once \mathbf{J}_{rj} , \mathbf{J}_{tj} , \mathbf{J}_{wj} , \mathbf{M}_1 , and \mathbf{M}_2 are obtained, the scattering matrix of the junction can be calculated.

V. CHARACTERISTICS OF PERIODIC WALL π JUNCTIONS

In order to design the feed waveguide, we have to investigate the dividing characteristics of the periodic wall π junction. Analyzing the dividing characteristics when the widths of both coupling and additional windows change, we have found that in-phase division with a desired power ratio can be obtained by controlling only the width w_1 of the coupling window and keeping the width w_2 of the additional one constant as shown in the next section. Therefore, w_2 is set to be a half of a_r so that the cross section of the window post is circular ($2t = 0$ in Figs. 3 and 5).

Fig. 7 shows characteristics of the junction as functions of the window width w_1 . The other parameters are listed in

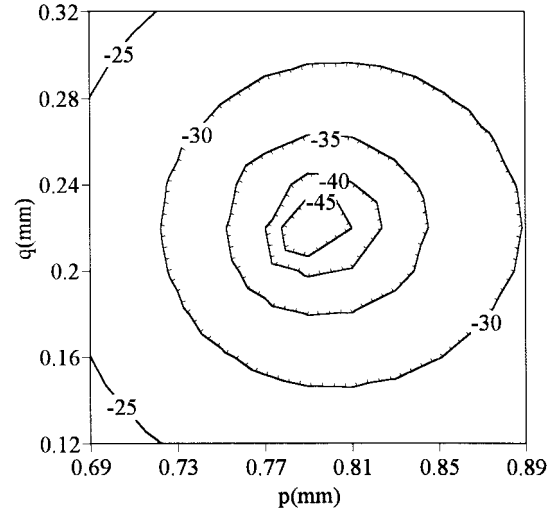


Fig. 8. Reflection (in decibels) as a function of post position ($w_1 = 2.45$ mm).

Table I. The post position (p, q) is determined to suppress the reflection, below -40 dB for each w_1 . In Fig. 7(a), the divided power becomes larger when the coupling window w_1 widens. The post should be placed close to the center ($p = 0$) of the feed waveguide, where the electric field is strong for larger w_1 in order to cancel strong coupling of the window. It is shown in Fig. 7(b) that $\angle S_{21}$ has a small positive value since the effect of a series inductance of the coupling window on the TE₁₀ mode transmission line is almost canceled by that of a parallel inductance of the reflection-canceling post [12].

Fig. 8 depicts the contour map of the reflection as a function of the post position (p, q). About 20% of the divided power $|S_{31}|^2$ is obtained for $w_1 = 2.45$ mm. The reflection is minimized for $p = 0.79$ mm and $q = 0.22$ mm. This figure reveals the requirement on the accuracy of the post position to suppress the reflection. An ± 70 -mm error is allowed in order to get reflection less than -30 dB.

VI. DESIGN

At first, we briefly present a design of the feed waveguide allowing us to divide uniformly in both amplitude and phase into all the radiating waveguides. The followings are some guidelines to set the geometry of the feed waveguide properly.

- 1) The distance h of the additional windows from the coupling ones is set to be about $0.3\text{--}0.4 \lambda_r$, where λ_r is the wavelength in the parallel plates.
- 2) The spacing a_r of the periodic boundary walls is set to be about $1.2\text{--}1.4 \lambda_r$.
- 3) The equivalent width a_e of the feed waveguide is determined so that the guide wavelength λ_f of the feed waveguide is a bit shorter than a_r in order to compensate a $5\text{--}6^\circ$ progression of the transmission phase $\angle S_{21}$ of the junction, as shown in Fig. 7(b).

The window width and the post position of each junction can be defined by the following procedures. N junctions are cascaded as shown in Fig. 9 and they are numbered from the termination to the input port. The desired divided power

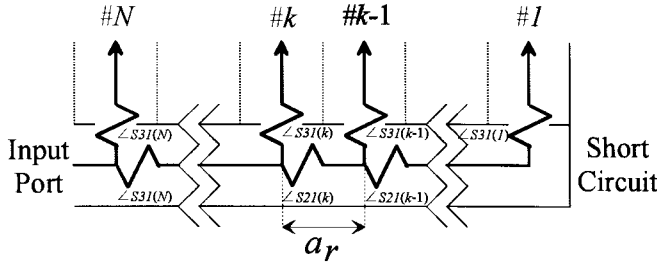


Fig. 9. Cascade of junctions.

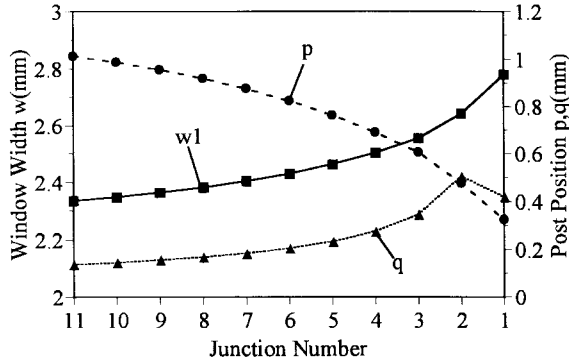


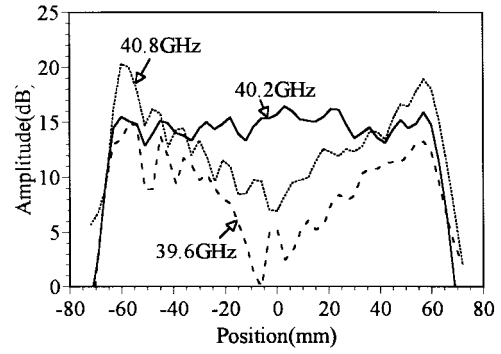
Fig. 10. Parameters of the feed waveguide.

$|S_{31}|^2$ of the junction $\#k$ ($k = 1 - N$) is $1/k$. The recursive equation for matching the phase $\angle S_{31}$ (deg) of the divided wave between the junctions $\#k - 1$ and $\#k$ is derived

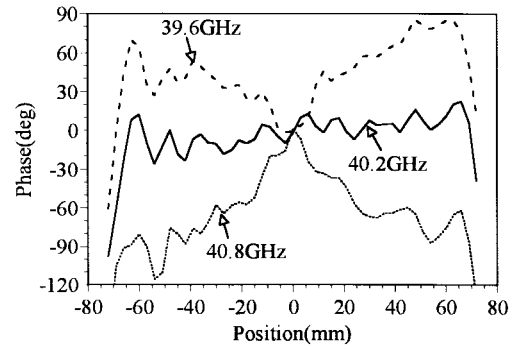
$$\angle S_{31}(k) - 360 = \angle S_{21}(k) - a_r / \lambda_f \times 360 + \angle S_{31}(k-1). \quad (10)$$

According to the guideline 3) and (10), the phase difference of the divided wave between two adjacent junctions equals a few degrees and it is not necessary to control the phase $\angle S_{31}$ of the divided wave among the junctions in a wide range. $\angle S_{31}(k)$ of all the junctions can be evaluated by using (10) according to the design procedure described in [12], once $\angle S_{31}(1)$ of the junction $\#1$ (matching junction) is given. Then all the parameters of the junction $\#k$ are obtained for $|S_{31}|^2(k)$ and $\angle S_{31}(k)$. A matching junction is designed for terminating the feed waveguide. Two posts are placed in the feed waveguide acting as an equivalent shorted circuit. The leakage from the two posts, i.e., the transmission S_{21} , is confirmed to be less than -30 dB. The coupling width $w_1(k)$ of each junction is determined from $|S_{31}|^2(k)$, $\angle S_{31}(k)$, and $\angle S_{21}(k)$ (shown in Fig. 7). Then post position $p(k)$ and $q(k)$ are obtained by using Fig. 7(a).

We design a cascade of eleven periodic wall π junctions. In this design, a_r / λ_f is fixed as 364.93° . Fig. 10 shows the parameters of the junctions. When the junction gets closer to the termination side, the width w_1 of the coupling window increases and the transverse position p of the reflection-canceling post shifts the center of the feed waveguide so that the divided power $|S_{31}|^2$ becomes larger. The matching junction $\#1$ needs to compensate for 20° difference of the divided phase of the adjacent junction $\#2$ by changing $w_1(1)$.



(a)



(b)

Fig. 11. Near-field distribution along the feed waveguide. (a) Amplitude. (b) Phase.

VII. EXPERIMENTAL RESULTS

We have designed and fabricated a 40-GHz band model antenna using the feed waveguide. Fig. 11 shows the near-field distributions along the feed waveguide, which reflect on the dividing characteristics of the feed waveguide. The distribution is almost uniform in amplitude and phase at 40.2 GHz. The phase is tapered at the lower and higher frequencies due to the change in the wavelength in the feed waveguide. The amplitude was expected to be strong near the feed point (center) at the higher frequency, but it is actually strong near the edges of the feed waveguide. This may occur because of wave propagation along the array of the waveguide posts in front of the coupling windows.

The frequency bandwidth of the feed waveguide has been evaluated experimentally since we were not able to analyze the whole region. Fig. 12 demonstrates the frequency characteristic of a gain calculated by using the measured one-dimensional near-field distributions in Fig. 11. The 1-dB down bandwidth is about 3% (from 39.6 to 40.8 GHz).

VIII. FUTURE STUDY

Practical realization of the desired performances of this feed waveguide depends on the accuracy of the post position. A numerical control machine to make via holes has typically $\pm 50\text{-}\mu\text{m}$ error in terms of the whole position. This position error of the posts composing the post-wall waveguide modifies the wavelength. The position error of the reflection-canceling post produces small reflection from each junction. As for a reflection-canceling post, an allowable error to suppress the reflection of a junction below -30 dB is estimated to be about

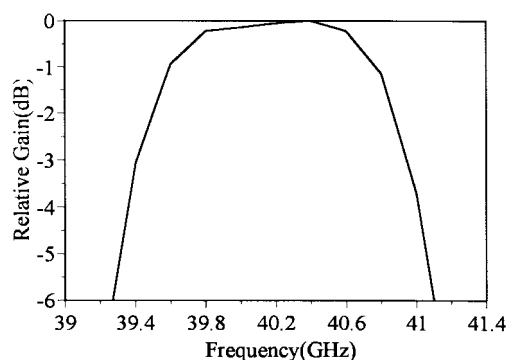


Fig. 12. Gain of near-field distribution.

$\pm 15 \mu\text{m}$ in a 77-GHz band model for a car collision avoidance radar. Special fabrication techniques to reduce the position error will be required. We will investigate in detail the effects of this position error on the dividing characteristics, the bandwidth and the center frequency of the feed waveguide by analyzing the whole region. We will also evaluate conductor losses due to roughness of the surfaces of the metalized via holes.

After overcoming many problems still to be solved and realizing the desired performance of the feed waveguide, we will have to compare a planar antenna using this feed structure with a conventional antenna described in [10].

IX. CONCLUSION

The authors have proposed a single-layer feed waveguide to excite a plane TEM wave for parallel-plate slot arrays. The propagation constant of the post-wall waveguide has been calculated as a function of the spacing and the diameter of the posts. The inner field in the post-wall waveguide leaks intensively through the wider windows so that the equivalent PEC walls should be shifted outside. The attenuation is very small for the post spacing up to twice as long as the post diameter. The unit component named a periodic wall π junction has been analyzed by Galerkin's method of moments. The amplitude and the phase of the divided wave can be controlled by the width of the coupling window. A cascade of junctions has been designed to obtain an uniform division, which is confirmed by measurements on a 40-GHz band model. The 1-dB down bandwidth is about 3% in terms of the gain of the feed waveguide.

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Dr. Ando received the Young Engineers Award of IECE Japan in 1981 and the Achievement Award and the Paper Award from IEICE Japan in 1993. He also received the fifth Telecom Systems Award in 1990 and the eighth Inoue Prize for Science in 1992.