The Problem and Treatment of DC Offsets in FDTD Simulations

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Abstract—This paper discusses the causes of and some solutions to the commonly observed problem of dc field offsets in finite-difference time-domain (FDTD) simulations. DC electric and magnetic field offsets are shown to be valid calculated responses of the modeled systems, resulting from interaction between the turn-on characteristics of the source and the properties of the models. The dc offsets may be avoided in the time domain by tailoring the source waveforms or in the frequency domain by post-processing the FDTD output.

Index Terms—Finite-difference time-domain (FDTD) methods.

I. INTRODUCTION

IN recent years, the finite-difference time-domain (FDTD) method has become a popular tool for solving problems involving Maxwell's equations [1]. Although the method is versatile and conceptually straightforward, an FDTD simulation must be carefully designed in order to yield meaningful results. In particular, if a highly conducting model has a closed current path, physically correct dc currents that do not decay appreciably with time may be induced by the time-varying source fields. These nonoscillating currents produce a constant magnetic field in the near field of the object, which may lead to interpretation errors in both the time and frequency domains [2].

II. EXAMPLE OF DC OFFSET FOR AN INFINITE CIRCULAR METAL CYLINDER

A dc magnetic field offset can be observed in the results of an FDTD analysis of a perfectly conducting infinite circular cylinder illuminated by a plane wave. The cylinder is 7.5 cm (20 Yee cells) in diameter, modeled with a square cell size of 0.375 cm ($\lambda/20$ at 4 GHz) in a two-dimensional (2-D) model space of 56×56 cells. Each time step is 6.25 ps. Fig. 1 shows time histories of the calculated fields at a point four cells in front of the cylinder (for a frontally incident plane wave source). For case (a), the time dependence of the electric field of the source is a raised cosine pulse with a 2 V/m peak and a 4-GHz bandwidth (half-width half-maximum); for case (b) the source is an unramped 4-GHz continuous wave (CW), with 1 V/m peak.
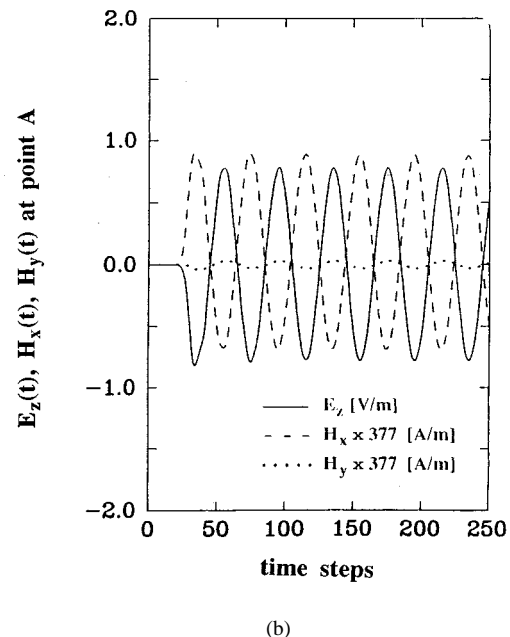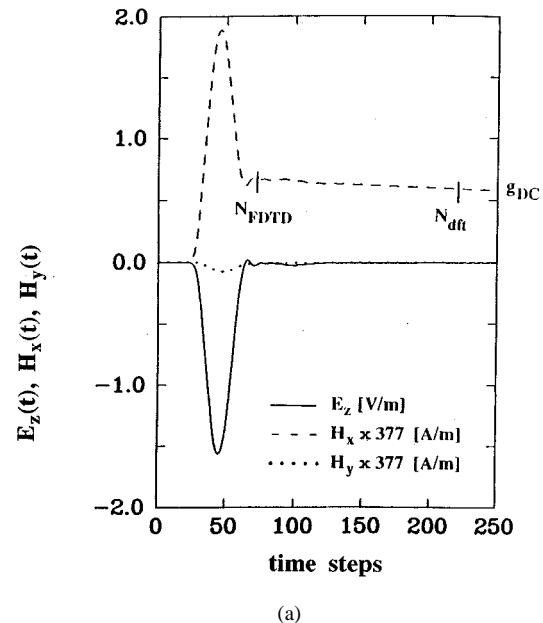
(a)

(b)

Fig. 1. The fields calculated by FDTD at a point one-fifth of a wavelength in front of a perfectly conducting circular cylinder illuminated from the front by a TM-polarized plane wave with time history of (a) raised cosine pulse and (b) single frequency (CW) unramped sine wave. The coordinate system is oriented such that the x-axis is in a direction tangential to the cylinder's surface at the front, and the y-axis is normal to the front surface. The z-axis is parallel to the axis of the cylinder.
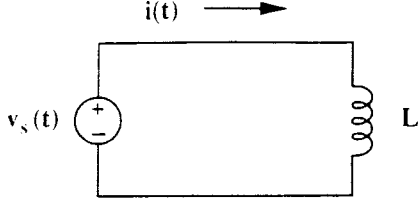
Fig. 2. Circuit model of an ideal voltage source across an inductor used to explain dc-offset phenomena in lossless FDTD simulations.

A dc offset in the tangential magnetic field (H_x) is observed for the cylinder illuminated by a TM-polarized plane wave [in which the incident electric field is oriented in the axial (z) direction]. This offset occurs with both pulsed and CW TM-polarized excitation, as shown in Fig. 1(a) and (b). The offset is not observed in any field component for TE-polarized excitation nor for TM polarized waves incident on a nonconducting cylinder. The reason is that the TM-polarized plane wave induces a permanent axial dc current in the perfectly conducting cylinder, but such an axial current will not be set up with a TE-polarized source, and will not survive in an imperfectly conducting object.

III. CIRCUIT THEORY ANALOG TO DC OFFSETS

To illustrate how dc offsets may be induced, consider a simple circuit composed of an ideal voltage source exciting an inductive load as shown in Fig. 2. The current through the inductor is given by

$$i(t) = \frac{1}{L} \int_0^t v_s(\tau) \, d\tau. \tag{1}$$

For sine excitation $v_s(t) = u(t)\sin(\omega t)$, (1) gives

$$i(t) = \frac{1}{\omega L} - \frac{1}{\omega L}\cos(\omega t). \tag{2}$$

Assuming the initial condition $i(0) = 0$, (2) shows that a dc offset is present in the circuit response. Further analysis shows that the dc offset is not required when loss is present in the system. It is reasonable to expect that similar phenomena may occur in FDTD simulations. The relationship between the circuit in Fig. 2 and the TM-illuminated cylinder is demonstrated in the next section.

IV. AVOIDING DC OFFSETS IN THE TIME DOMAIN

For pulsed excitations, the dc offset may be avoided by using a bipolar pulse with equal positive and negative values. This causes the dc offset that is established by the positive portion of the pulse to be removed by the negative portion of the pulse.

For CW computations, we have also found (in all our test cases) that the offset may be eliminated by multiplying the sinusoidal excitation by an appropriate ramp function. An example

is $v_s(t) = r(t)\sin(wt)$, where $r(t)$ is either a linear ramp or a raised cosine (RC) ramp given by

$$r(t) = \begin{array}{ll} 0, & t < 0 \\ 0.5[1 - \cos(\omega t/2\alpha)], & 0 \le t \le \alpha T \\ 1, & t > \alpha T \end{array} \tag{3}$$

where T is the period of the sine function, and α is the number of sine wave cycles during the ramp duration αT. This excitation has the desirable properties that both the function and its first derivative start at zero and are continuous for all values of α. The choice of ramp function can be evaluated by the simple circuit analogy of an ideal voltage source across an inductor. For the linear ramp this gives

$$i(t) = \frac{1}{\omega L}\left(\frac{\sin(2\pi\alpha)}{2\pi\alpha}\right) - \frac{1}{\omega L}\cos(\omega t) \tag{4a}$$

and for the RC ramp

$$i(t) = \frac{1}{2\omega L}\left(\frac{1 + \cos(2\pi\alpha)}{1 - 4\alpha^2}\right) - \frac{1}{\omega L}\cos(\omega t). \tag{4b}$$

The first terms in (4a) and (4b) give the value of the dc current offset for each excitation. Note that although the excitation function and its first derivative start at zero and are continuous, the dc offset is zero only for particular values of α. Fig. 3 shows these dc values for the linear and RC ramps, normalized to the magnitude of the offset produced by a step function $u(t)$ as a function of the ramp duration parameter α. As expected, the magnitude of the offset decreases as the ramps get longer. The RC ramp produces less offset than the linear ramp for values of α greater than about 1.5. Also, the dc offset is identically zero for certain values of α.

To test the similarity of this circuit model to the results for the TM-illuminated perfectly conducting cylinder, the FDTD simulations for the cylinder were repeated using the linear- and RC-ramped sine excitations. The normalized magnitude of the resulting dc offset in the circumferential magnetic field component are superimposed as dots in Fig. 3. There is excellent agreement (less than 5% difference) between the FDTD data and the results for the inductive circuit of Fig. 2; the change in sign of the offset is also predicted correctly. The results demonstrate a strong similarity between these two models, and show clearly how the dc offset may be controlled by the choice of excitation ramp function in FDTD simulations. A dc offset will not, of course, persist when a resistive loss is added to either model.

V. REMOVAL OF DC OFFSETS IN THE FREQUENCY DOMAIN

For CW excitation, the dc offset appears in the zero-frequency term only, so it is easily separated from the higher frequency terms in the frequency domain. For pulsed simulations, the FDTD simulation is stopped when the output pulse converges (after N_{FDTD} time steps), and this is often before the discrete Fourier transform (DFT) summation has been completed (N_{dft} time steps). Without a dc offset this does not cause any problems; the fields are assumed to be zero after the simulation is stopped, and the remaining terms in the DFT summation are zero. However, when the pulse has a dc offset [as shown in Fig. 1(a)], stopping the summation before it is
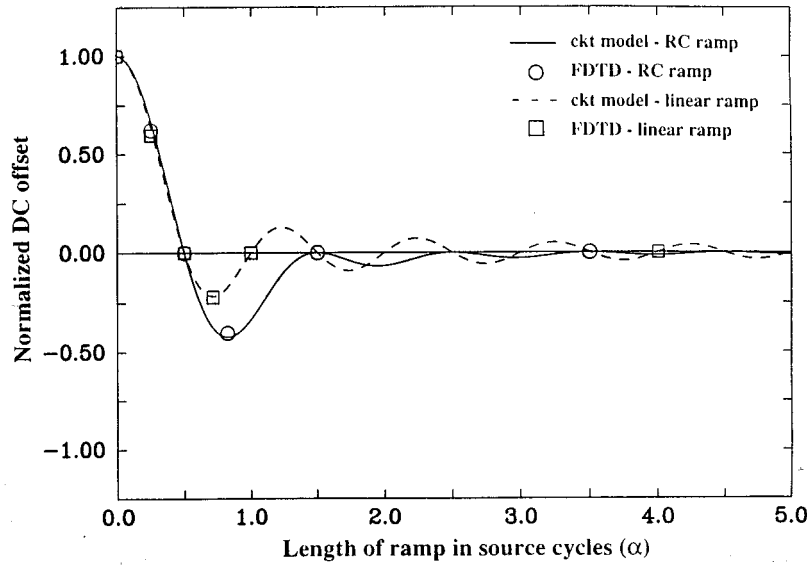
Fig. 3.   Variation of normalized magnitude of the dc offset of magnetic field caused by linear and raised-cosine ramp envelopes on a $\sin(\omega t)$ source. Values are plotted as functions of the ramp duration parameter a. Solid and dashed lines are for the inductive circuit model of Fig. 2. Discrete points are FDTD results for the TM-illuminated perfectly conducting cylinder.

completed gives erroneous results. An efficient way to handle complete the summation is to divide the Fourier sum into two summations

$$G(k\Delta f) = \Delta t \left\{ \sum_{n=1}^{N_{\text{FDTD}}} g(n\Delta t) \exp\left[\frac{-j2\pi k(n-1)}{N_{\text{dft}}}\right] \\ + g_{\text{dc}} \sum_{n=N_{\text{FDTD}}+1}^{N_{\text{dft}}} \exp\left[\frac{-j2\pi k(n-1)}{N_{\text{dft}}}\right] \right\}. \tag{5}$$

The first term in (5) is the summation over the time-varying portion of the pulse, $g(n\Delta t)$, up to the time the FDTD simulation is completed, N_{FDTD}. The second term is the summation over the dc portion of the pulse g_{dc} up to time the DFT summation has been completed N_{dft}.

The second term can be written as a finite geometric series and summed to get

$$G(k\Delta f) = \Delta t \left\{ \sum_{n=1}^{N_{\text{FDTD}}} g(n\Delta t) \exp\left[\frac{-j2\pi k(n-1)}{N_{\text{dft}}}\right] \\ + g_{\text{dc}} \frac{1 - \exp\left[\frac{-j2\pi k N_{\text{FDTD}}}{N_{\text{dft}}}\right]}{1 - \exp\left[\frac{-j2\pi k}{N_{\text{dft}}}\right]} \right\}. \tag{6}$$

The first summation is updated along with the FDTD simulation. The second term, which represents the summation over the dc portion of the pulse, is now a single term and is subtracted after the simulation is completed.

VI. CONCLUSION

This paper has demonstrated the occurrence of dc offsets in FDTD simulations. These offsets are shown to be due to the physical response of the modeled system to certain sources. These offsets may be avoided in the time domain by tailoring the incident waveform, for example, by using a bipolar pulse for broad-band simulations or a ramped sine wave for CW simulations. DC offsets may also be filtered from frequency-domain data by post processing the FDTD output.

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