

A Micromachined 585 GHz Schottky Mixer

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Abstract—Standard semiconductor fabrication processes have been used to form waveguide components for the submillimeter wavelength range. A 585 GHz fundamentally pumped Schottky mixer with record performance demonstrates this technology. It consists of an etched silicon horn, a diced waveguide, and a lithographically formed microstrip channel for the diode circuit. The block dimensions are precisely controlled and extremely sharp. The measured mixer noise temperature is 1200K (DSB), which is equivalent to the best result obtained with standard metal machining.

Index Terms—Micromachining, submillimeter-wave circuits.

I. INTRODUCTION

SUBMILLIMETER-WAVE mixers and multipliers have long been available for scientific applications such as radio astronomy, atmospheric studies, and chemical spectroscopy. However, there is growing interest in the use of submillimeter-wave heterodyne receivers for more broad applications such as compact range radars, ultra-wide band and secure communications, medical diagnostics, weapons and explosives detection, and biological and chemical toxin detection. Traditional technology has used whisker-contacted and discrete planar diodes that yield suitable noise temperatures but tend to be narrow band and very expensive. Recently, two groups have developed GaAs on quartz integration technologies that promise better sensitivity and wider fix-tuned bandwidth, as well as reduced costs [1]–[3]. However, the difficulty and expense of forming the split-block waveguide housings for these terahertz circuits continues to limit the application of this technology.

This letter describes the fabrication and testing of a split-block 585 GHz mixer housing by standard semiconductor fabrication processes. The essential mixer design has been described previously [4], [1]. The micromachining process yields exceptional control of the critical block dimensions and extremely sharp features. The resulting mixer performance is equivalent to the best 585 GHz Schottky mixers. Also, the assembly survives immersion in liquid nitrogen and this process is readily scaled to well above 1 THz.

II. OVERVIEW OF THE FABRICATION PROCESS

The block fabrication process presented here is a modified version of the process reported in several previous conference

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publications [5]–[7]. As in the previous work, our new process begins with the formation of a modified diagonal horn by selective crystal etching of a silicon wafer through a silicon dioxide masking layer. This etch creates a very suitable horn structure with easily controlled flare angle and aperture [8]. Next, a thin layer of photoresist is spun onto the wafer and exposed to mark the precise position of the waveguide. An automatic dicing saw is then used to slit-cut the waveguide with depth of 150 μm ($\pm 5 \mu\text{m}$) and width 205 μm ($\pm 2 \mu\text{m}$) for each half of the block. The photoresist and oxide layers are then removed.

The next step is to form the microstrip circuit channel that runs perpendicular to the waveguide. This is achieved with an ultra-thick photoresist known as SU-8 [9]. This resist can be exposed by standard UV lithography to depths of up to 1 mm. First, the horn structure is filled with SU-8 resist. Next, a layer of SU-8 is spun on the wafer that is significantly thicker than our desired channel depth of 60 μm . This resist is then exposed through a mask that protects the horn, waveguide and channel areas. Both pre- and post-exposure bakes are used. After the exposure, the wafer is developed to remove the unexposed resist and hard-baked to cure the remaining SU-8 into a plastic layer that remains as a permanent part of our mixer. This plastic is then lapped to the desired thickness on a commercial wafer lapping system. Lapping allows control of the depth of the channel to within $\pm 2 \mu\text{m}$ and eliminates any problem with the planarity of the original SU-8 surface. The width of the microstrip channel was 120 μm ($\pm 2 \mu\text{m}$).

Alignment grooves are then diced into the wafer (200 μm deep by 400 μm wide), the sample is coated with metal by a combination of sputtering and electroplating and the individual components are diced. Both the dicing and alignment grooves are patterned in the SU-8 layer to facilitate proper alignment on the wafer. The processing of one 3-in wafer took approximately two weeks of graduate student effort and yielded twelve complete waveguide pairs. The result is shown in Fig. 1. Note that the features are much sharper than is possible with traditional machining and the fixed backshort is defined with lithographic precision.

III. MIXER ASSEMBLY AND TESTING

To assemble the mixer a quartz microstrip circuit with an IF filter, a waveguide probe and an integrated GaAs diode is placed in the microstrip channel [1]. The diodes have a nominal anode diameter of 0.9 μm and an epitaxial layer doping of $4 \cdot 10^{17} \text{ cm}^{-3}$. The measured dc parameters for this diode were an ideality factor $\eta = 1.32$, a saturation current $I_{\text{SAT}} = 3 \cdot 10^{-13} \text{ A}$, and a series resistance $R_s = 11 \Omega$. The zero bias junction capacitance was calculated to be 1.5 fF per anode based on the anode diameter and the epitaxial layer doping. Bond wires attached to

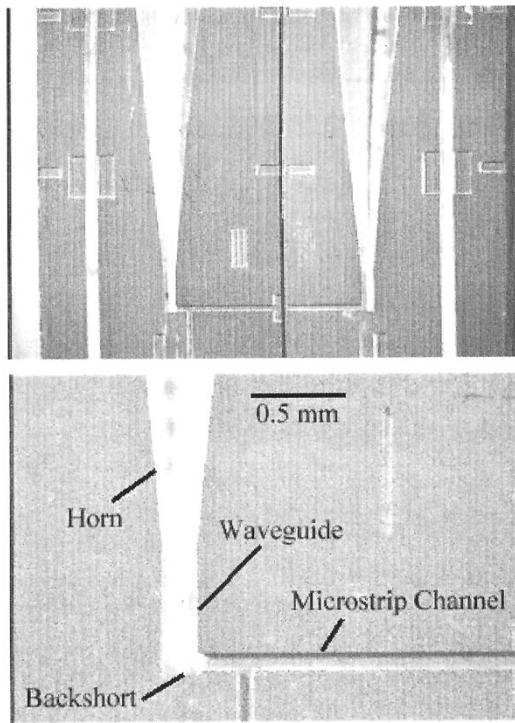


Fig. 1. Two views of the 585 GHz split-block mixer, showing the flared horn, waveguide, and microstrip channel. Note the extremely sharp features.

the circuit are then attached to the block for the IF return and to the center pin of the coaxial IF connection. Metal shims are placed in the alignment grooves and these shims guide the two halves precisely into place. This yields excellent alignment and the flat SU-8 surfaces formed by lapping yield no visible gap between the halves.

The mixer is then tested by the same method as has been described previously [4]. A molecular gas laser provides an LO source at 585 GHz and a hot/cold load source is used as a calibrated signal. The LO and signal are spatially combined in a diplexer and coupled to the horn through an off-axis parabolic mirror. The lowest system noise temperature measured was 1700K and a graph of the system noise temperature versus the input noise temperature of the IF amplifier indicated a mixer noise temperature of 1200K and conversion loss of 8 dB (all DSB). The system noise temperature is plotted versus LO power in Fig. 2. The mixer requires about 1 mW of LO power for optimum performance and the performance is still quite good down to 0.2 mW. These are essentially the same values obtained when a similar integrated mixer circuit was tested in a traditionally machined metal block [1]. The antenna pattern of the micromachined horn is shown in Fig. 3. There is a slight asymmetry in the beam but this can be corrected by adjusting the depth of the horn etch.

IV. CONCLUSIONS

We have fabricated split block mixer housings for 585 GHz through standard semiconductor processing techniques including crystallographic silicon etching, ultra-thick photoresist, automatic dicing and wafer lapping. The results indicate

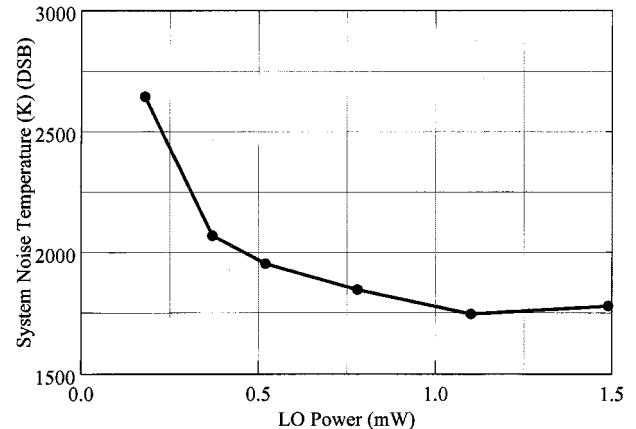


Fig. 2. System noise temperature versus LO power. The best measured result was 1700K (DSB).

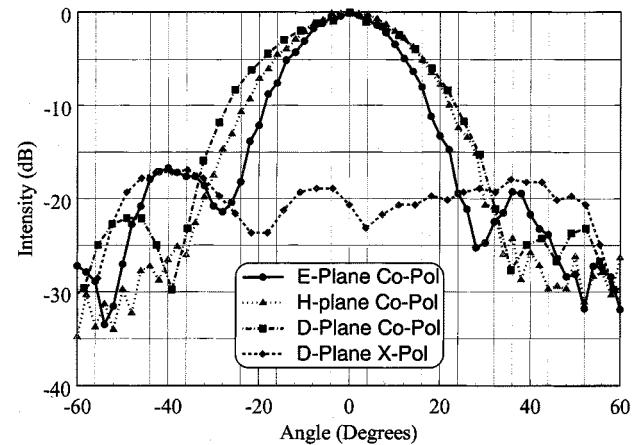


Fig. 3. Measured antenna pattern of the 585 GHz mixer. The slight asymmetry can be removed with a better selection of the silicon etch depth.

better dimensional control and sharper features than have been demonstrated with traditional machining. The mixers have been RF tested and yield essentially the same performance as the traditional blocks with a diagonal horn antenna. This is the first demonstration of a micromachined submillimeter-wave mixer with such exceptional performance. This process is readily scaled to frequencies above 1 THz and is suitable for large-scale manufacturing. For example, a single eight inch silicon wafer would yield over 80 600-GHz mixer housings. Finally, since the blocks have survived rapid immersion in liquid nitrogen with no degradation this technology can potentially be used for superconducting (SIS and HEB) mixers. Thus, this new micromachining process makes possible the rapid prototyping of waveguide circuits throughout the terahertz band while also greatly reducing costs for mass production.

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